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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of: )  
MATSUMOTO et al. )  
Serial No. 09/818,743 )  
Filed: March 27, 2001 )  
For: SEMICONDUCTOR DEVICES AND )  
METHODS FOR MANUFACTURING )  
THE SAME )

Group Art Unit: 2823

Examiner: Collins, Deven M.

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**RESPONSE TO RESTRICTION REQUIREMENT**

Assistant Commissioner for Patents  
Washington, DC 20231

Dear Sirs:

In response to the Election/Restriction Requirement dated May 30, 2002, the response being due by Monday, July 1, 2002 (June 30 was a Sunday), please enter and consider the following.

Applicant hereby elects to prosecute Group I, claims 8-21, drawn to methods of manufacturing a semiconductor device.

Examination on the merits is respectfully requested. It is believed that no fees are due in connection with this paper. If however, any such fees are due, the Commissioner is hereby authorized to charge payment of the fees associated with this paper to Deposit Account No. 50-0585. A copy of this paper is enclosed.

Respectfully submitted,

Alan S. Raynes

Reg. No. 39,809

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Dated: July 1, 2002

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**CERTIFICATE OF MAILING**

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to: Assistant Commissioner for Patents, Washington, D.C. 20231 on July 1, 2002.

Alan S. Raynes

July 1, 2002  
(Date)